



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Kazunori SAKURAI et al.

Group Art Unit: 2824

Application No.: 09/843,924

Examiner: Beth E. Owens

Filed: April 30, 2001

Docket No.: 109182

For: METHOD FOR FORMING BUMP, SEMICONDUCTOR DEVICE AND METHOD FOR
MAKING THE SAME, CIRCUIT BOARD, AND ELECTRONIC DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached.

1. An english-language abstract of the non-English language reference is attached hereto.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

Tarik M. Nabi
Registration No. 55,478

JAO:TMN/tje

Date: November 8, 2004

OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

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Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 109182		APPLICATION NO. 09/843,924	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)							
				APPLICANT(S) Kazunori SAKURAI et al.			
				FILING DATE April 30, 2001		GROUP 2824	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME		CLASS	SUB CLASS
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY		CLASS	SUB CLASS
1.		JP 54-105962-A (with abstract)	08/20/1979	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
EXAMINER				DATE CONSIDERED			